

Electronic Acknowledgement Receipt

EFS ID:	1205793
Application Number:	10813837
Confirmation Number:	6047
Title of Invention:	Voltage reference generator circuit using low-beta effect of a CMOS bipolar transistor
First Named Inventor:	Akhil K. Garlapati
Customer Number:	22120
Filer:	Nicole Teitler Cave/Katrina Prati
Filer Authorized By:	Nicole Teitler Cave
Attorney Docket Number:	026-0044
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		026-0044_Response_20060918.pdf	617009	yes	10

	Multipart Description		
	Doc Desc	Start	End
	Amendment After Final	1	1
	Applicant Arguments/Remarks Made in an Amendment	2	10

Warnings:

Information:

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.